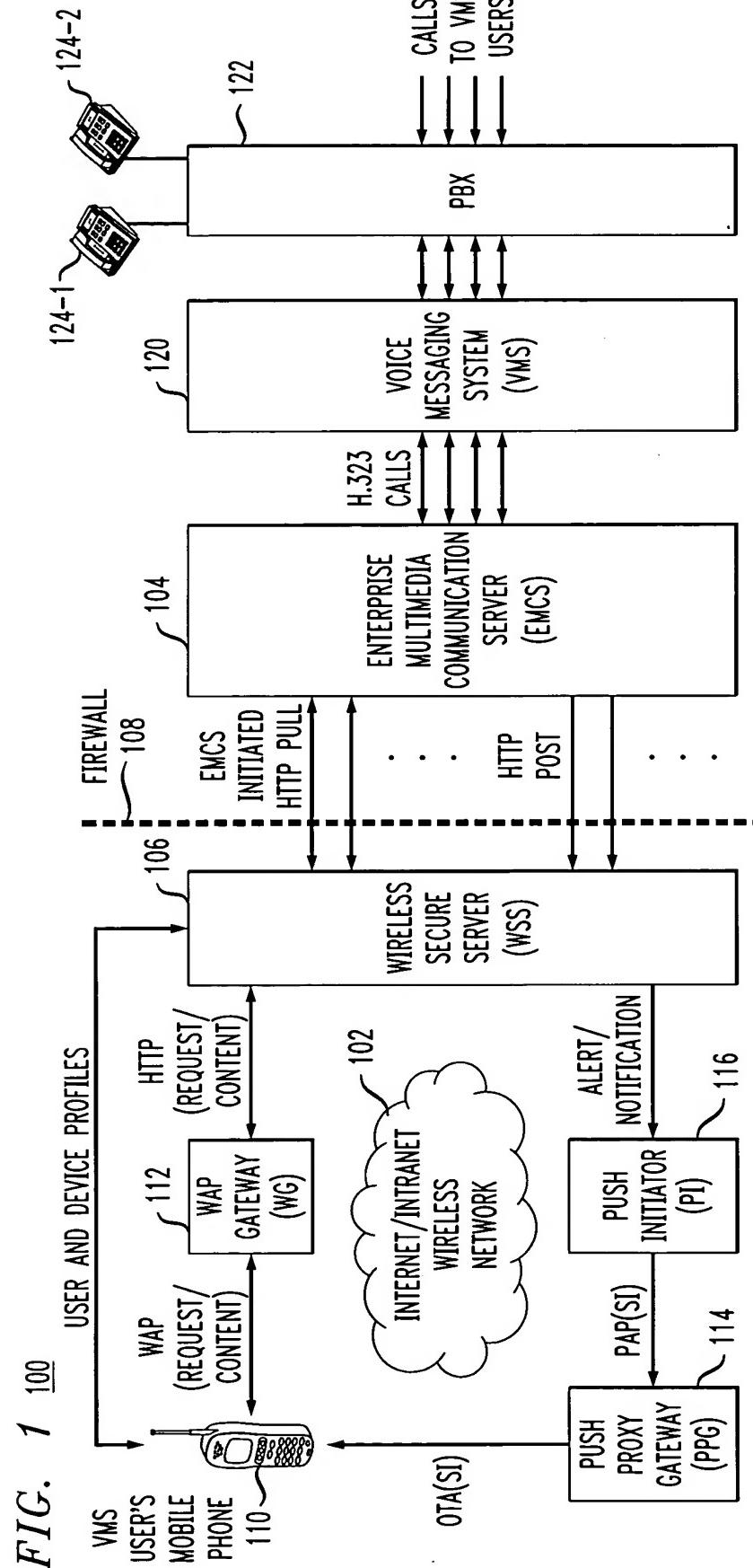




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```

graph TD
    Processor[PROCESSOR] <--> NI[NETWORK INTERFACE(S)]
    Processor --> Memory[MEMORY]
    Memory --> NI
    NI <--> Network[TO/FROM NETWORK]

```

The diagram illustrates a computer architecture. At the top is a box labeled "NETWORK INTERFACE(S)". Below it are two boxes: "PROCESSOR" on the left and "MEMORY" on the right. Double-headed arrows connect the Processor to the Network Interface and the Memory to the Network Interface. A single-headed arrow points from the Processor to the Memory. To the left of the Processor is a label "202" with a curved arrow pointing to it. To the right of the Memory is a label "204" with a curved arrow pointing to it. Above the Network Interface is a label "206" with a curved arrow pointing to it. To the right of the Network Interface is a label "TO/FROM NETWORK" with a double-headed arrow pointing to the interface.

FIG. 2